



Mail Stop Patent
Attorney Docket No. 82522

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ram Singh RANA et al.

Serial No.: 10/624,679

Group Art Unit: 2817

Date Filed: July 23, 2003

For: **LOW SUPPLY-SENSITIVE AND WIDE TUNING-RANGE CMOS LC-TANK
VOLTAGE-CONTROLLED OSCILLATOR MONOLITHIC INTEGRATED CIRCUIT**

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith for filing in the U.S. Patent and Trademark Office is the following:

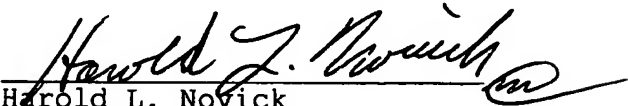
- (1) Transmittal Letter;
- (2) Information Disclosure Statement Transmittal Letter;
- (3) PTO Form 1449; and
- (4) Sixteen (16) references.

The Commissioner is hereby authorized to charge any deficiency or credit any excess to Deposit Account No. 14-0112.

Respectfully submitted,

NATH & ASSOCIATES PLLC

By:


Harold L. Novick
Registration No. 26,011
Customer No. 20529

Date: October 22, 2003
NATH & ASSOCIATES PLLC
1030 15th Street N.W., 6th Floor
Washington, D.C. 20005
(202) 775-8383
HLN/be



Mail Stop Patent
Attorney Docket No. 82522

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ram Singh RANA et al.

Serial No.: 10/624,679

Group Art Unit: 2817

Date Filed: July 23, 2003

**For: LOW SUPPLY-SENSITIVE AND WIDE TUNING-RANGE CMOS LC-TANK
VOLTAGE-CONTROLLED OSCILLATOR MONOLITHIC INTEGRATED CIRCUIT**

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

An Information Disclosure Statement is submitted herewith pursuant to 37 C.F.R. §1.97-1.98. Please note the following particulars:

[NOTE: One only of items a, b, c, and d must be checked.]

- [X] a. The enclosed statement is being filed within three months of the filing date of a national application, or within three months of the date of entry into the national stage as set forth in 37 C.F.R. §1.491 in an international application, or before the mailing date of a first Office Action on the merits, whichever event occurs last.
- [] b. The enclosed statement is being filed after a first action on the merits but before the mailing date of a final action under 37 C.F.R. §1.113, or a notice of allowance under 37 C.F.R. §1.311.

The enclosed statement is accompanied by [check one]:

- [] i. a certification in part (e) below as specified in 37 C.F.R. §1.97(e), or
- [] ii. a check in the amount of \$180.00 required by 37 C.F.R. §1.17(p).
- [] c. The enclosed statement is being filed after the mailing date of a final action under 37 C.F.R. §1.113, or a notice of allowance under 37 C.F.R. §1.311, but before payment of the issue fee.
- [] d. The enclosed statement is being filed pursuant to 37 C.F.R. §1.97(i), for placement in the file.

- ☐ e. Certification [Check one] [Certification is required only if box (b)(I) or box (c) is checked.]

☐ I hereby certify that each item of information contained in the enclosed Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement,

or

☐ I hereby certify that no item of information in the enclosed Information Disclosure Statement herewith was cited in a communication from a foreign patent office in a counterpart foreign application, or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. §1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

or

☐ Appropriate certification is attached.

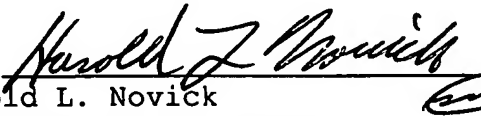
- ☒ f. If no check is enclosed and a fee is due in connection with this communication or if the check enclosed is insufficient, the Commissioner is authorized to charge any fee or additional fee due in connection with this, communication to Deposit Account No. 14-0112.

Copies of the documents are attached herewith with a completed Form PTO-1449. The Examiner is respectfully requested to cite the documents listed on the attached Form PTO-1449 in the next Office Action. In so doing, the Examiner is respectfully requested to initial in the space adjacent to the listing of each document on the Form PTO-1449, and return a copy of the initialed Form PTO-1449 with the next communication to Applicants, to confirm that these documents have been considered by the Examiner and made of record in this application.

If the Examiner has any questions or wishes to discuss this application, kindly telephone the undersigned at the below-listed number.

Respectfully submitted,

NATH & ASSOCIATES PLLC

By: 
Harold L. Novick
Registration No. 26,011
Customer No. 20529

Date: October 22, 2003
NATH & ASSOCIATES PLLC
1030 15th Street, N.W., 6th Floor
Washington, D.C. 20005
Tel. (202) 775-8383
Fax. (202) 775-8396
HLN/be



FORM PTO-1449

INFORMATION DISCLOSURE CITATION

Atty Docket
82522Serial No.
10/624,679Applicant
Ram Singh RANA et al.Filing Date
July 23, 2003Group Art Unit
2817

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Issue Date	Name	Class	Sub-Class	Filing Date
<i>D</i>	AA	5,959,504	9/28/99	Hongmo WANG	—	—	3/10/98
<i>D</i>	AB	6,225,871 B1	5/1/01	Hwey-Ching CHIEN	—	—	2/7/00
<i>D</i>	AC	6,469,586 B1	10/22/02	ROGERS et al.	—	—	8/25/00
<i>D</i>	AD	6,353,368	3/5/02	Kamran IRAVANI	—	—	11/9/99
<i>D</i>	AE	6,037,842	3/14/00	BRYAN et al.	—	—	4/21/98
<i>D</i>	AF	6,104,254	8/15/00	Kamran IRAVANI	—	—	11/18/97
<i>D</i>	AG	5,936,476	8/10/99	Kamran IRAVANI	✓	—	11/18/97
<i>D</i>	AH	6,249,191 B1	6/19/01	Leonard FORBES	—	—	11/23/98
<i>a</i>	AI	5,635,880	6/3/97	Anthony BROWN	—	—	3/14/96
<i>o</i>	AJ	6,100,770	8/8/00	LITWIN et al.	—	—	9/9/98
<i>o</i>	AK	6,288,616 B1	9/11/01	Zhenhua WANG	—	—	5/16/00

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Translation
	AL						
	AM						
	AN						
	AO						
	AP						

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>D</i>	AR		M. Tiebout, "Low-Power Low-Phase-Noise Differentially Tuned Quadrature VCO Design in Standard CMOS", IEEE J. Solid-State Circuits, Vol.36, No. 7, July 2001
<i>N BT</i> <i>CONFIDENTIAL</i>	AS		L. Forbes, M. Cheng and J. Zhou, "Simulation of Phase Noise Generated by white noise in 1.7GHz CMOS LC Oscillator", Electronics Letters, Vol. 36, No. 23, 9 th November 2000
<i>o</i>	AT		J. Bhattacharjee, D. Mukherjee, E. Gebara, Su. Nuttinck, and J. Laskar, "A 5.8 GHz Fully Integrated Low Power Low Phase Noise CMOS LC VCO For WLAN Applications", 2002 IEEE MTT-S Digest

Examiner

MUS

Date Considered

12/1/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609.
 Draw line through citation if not in conformance and not considered.
 Include copy of this form with next communication to Applicant.

NO COPY



FORM PTO-1449

INFORMATION DISCLOSURE CITATIONAtty Docket
82522Serial No.
10/624,679Applicant
Ram Singh RANA et al.Filing Date
July 23, 2003Group Art Unit
2817

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Issue Date	Name	Class	Sub-Class	Filing Date
6	BA	6,107,893	8/22/00	Leonard FORBES	—	—	12/2/98
9	BB	6,011,443	1/4/00	CHEN et al.	—	—	7/16/98
	BC						
	BD						
	BE						
	BF						
	BG						
	BH						
	BI						
	BJ						
	BK						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Translation
	BL						
	BM						
	BN						
	BO						
	BP						

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	BQ		
	BR		
	BS		

Examiner

MCJ

Date Considered
1/11/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609.
Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to Applicant.